

# Documentation for:

# YED/A429/R1-DAT1-10V(Lattice FPGA)

# ARINC 429 to 1-channel Analogue Converter



## **YED Avionics Limited**

Park House 10, Park Street Bristol Avon BS1 5HX UK

*Tel:* +44 (0) 117 907 4761 E-mail: support@yed.com Web: www.yed.com

Version 1.0

Documentation for: YED/A429/R1/DAT1-10V Converter

## **Amendment Page**

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#### 1 Introduction

This manual contains specification data, installation and instructions for the YED/A429-R1-DAT1-10V, ARINC-429 to Analogue converter.

The function of this converter is to monitor a single ARINC 429 data bus for a selected ARINC 429 label and to translate the value contained within bits 28 thru 17 into a 0 to 10V DC output.

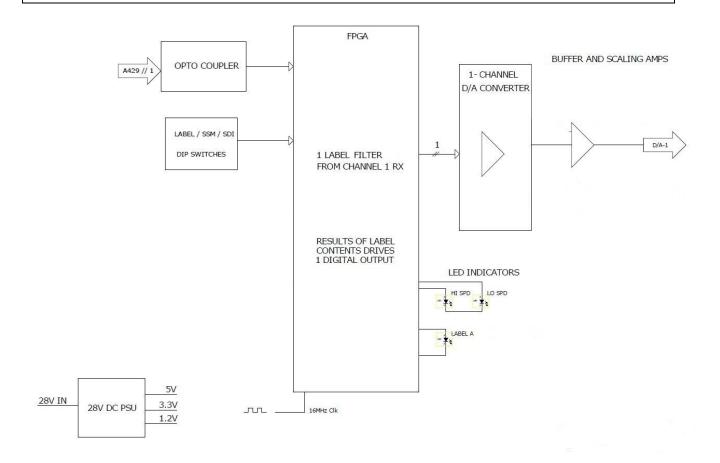
The ARINC 429 receiver is coupled to the data bus via an Optocoupler to maintain electrical isolation and EMI immunity.

This converter consists of a single channel ARINC 429 receiver and decoder that drive a D/A Converter configured to produce a 0-10V DC output for the received ARINC 429 Label. The ARINC 429 Label is selected by means of surface mounted switches on the printed circuit board. When the specified label is received the analogue output channel is updated to reflect the value contained within the received ARINC 429 data word. The analogue output is latched and will maintain the value of the last received data word until new data is received.

The converter can receive high or low speed ARINC 429 data automatically without user intervention. On the PCB there are two LED indicators that display the bit rate of the received ARINC 429 as Hi or Lo bit rates. In addition there is an LED indicator that indicates the reception of the selected ARINC 429 Label.

The unit is powered from an external 28 VDC (18-60V) nominal supply with internal current and thermal (102 °C) fuse. The power inputs are also reverse polarity protected and incorporate all the standard YED EMC/EMI surge protection techniques including a power supply monitoring device, which will cause the system to reset in the event of any problems with the main supply.

A block diagram of the main components of the converter is shown overleaf.



#### **BLOCK DIAGRAM OF CONVERTER INTERNALS**

#### 1.1 Firmware

This design is based upon a Field Programmable Gate Array (FPGA) only. There is no microprocessor and therefore RTCA DO-178 certification is not required for this product.

### 1.2 Environmental, Airworthiness and EMC

The YED/A429-R1-DAT1-10V Converter has been designed to meet DO-160D test categories listed later in this manual.

The unit has also been subjected to an Explosive Decompression test from 15,000 feet to 50,000 feet in a period of less than 100mS without effect. See Annex 1.

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## 1.3 Specification

The YED/A429-R1-DAT1-10V has the following features: -

#### **Physical**

The YED/A429-R1-DAT1-10V attaches to the airframe via four mounting holes. See paragraph titled "Enclosure Outline Drawing" for further details.

The enclosure is a CNC machined box with Anodised and an Alocrom 1200 finish.

Weight...... 300 grams (approx.)

**Electrical** 

Reverse polarity protected.

**Indicators (on PCB)** 

LED bit rate reception:..... Hi Speed/Low Speed

**ARINC 429 Input** 

Number of receivers...... 1

Input is via opto-coupler.

**Analogue outputs** 

Number of Analogue outputs...... 1

(Other output scale factors available)

Channel 1 output ...... Channel 1 DC output, 0-10V DC

Bit field of interest ...... 28..17

ARINC 429 Labels filtered (Any 1 from 256)

Channel 1: ...... User selectable Label via SMD switches

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**ARINC 429 SSM and SDI filtering** 

SSM filtering for channels 1: ..... 11,10,01,00 or XX (Don't care) via SMD switches

SDI filtering for channels 1,2 & 3: ..... 11,10,01,00 or XX (Don't care) via SMD switches

Connector

Industry Standard D15 sub-miniature socket.

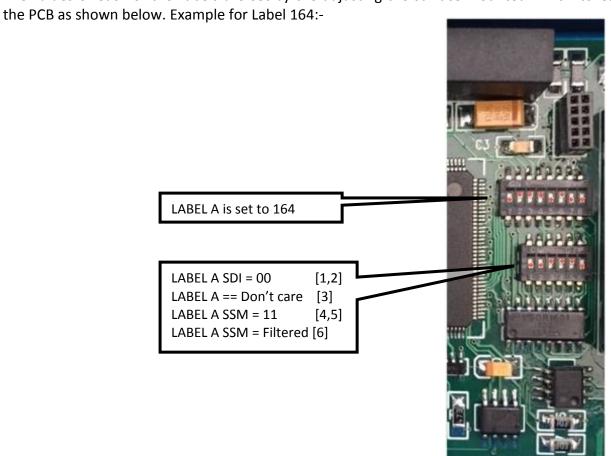
**Environmental** 

Operating temperature range..... -40 to +85 degrees °C

## 2 Configuration of converter

## 2.1 Description of setting Labels

The values of each of the Labels are set by the adjusting the surface mounted DIP switches on



## 2.2 Label switch description

Labels are coded in Octal. Red spot denotes a switch set to ON.

LAB-L1	LAB-L2	LAB-L3	LAB-L4	LAB-L5	LAB-L6	LAB-L7	LAB-L8	LABEL
OFF	ON	ON	ON	OFF	ON			164

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## 2.3 SDI and SSM switch description

The SDI and SSM switch is coded as shown below.

SDI-09	SDI-10	DON'T CARE	SSM-30	SSM-31	DON'T CARE
OFF	OFF	ON	ON	ON	OFF

SDI-09 refers to Bit 09 of the ARINC 429 word

SDI-10 refers to Bit 10 of the ARINC 429 word

SDI- Don't care means ignore SDI-10 and SDI-9 settings.

SSM-31 refers to Bit 31 of the ARINC 429 word

SSM-30 refers to Bit 30 of the ARINC 429 word

SSM- Don't care means ignore SSM-31 and SSM-30 settings.

Don't care means that there will not be any filtering of the respective SDI and/or SSM fields.

#### 2.4 ARINC 429 Label and data word format

A typical ARINC 429 data word (Label 164 – Rad. Alt.) is shown below.

32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
P	SS	SM	S					D	ATA									PΑ	D		FTI	SI	Ι	34			17557	BEL 64)			
																								0	0	1	0	1	1	1	0
No	Note: When Bit 11 (Functional Test Inhibit) is a "1", a functional test should not be performed.												4			6			1												

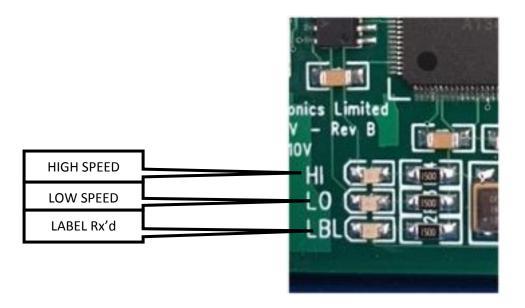
Radio Height Word

- Starting at Bit-32 is PARITY
- Bits 31 & 30 are the SSM filed, which indicates the status of the data.
- Bit-29 is the sign bit of the data.
- Bits28 thru 17 is the data field for this type of data word.
   The SDI field on bits 10 & 9 is shown as '00' for this particular data word.
- The Label field shown here as 164.

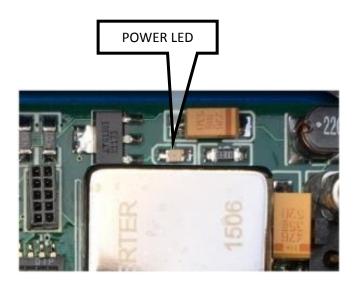
## 2.5 LED indication showing reception of Labels A and ARINC 429 bit rate

The image below shows the area of the printed circuit board that contains Label reception status LED indicators.

The bit rate of the received ARINC 429 data can be identified by inspection the LEDs as shown below. If an LED is lit then the label is being received and decoded.



## 2.6 LED Power showing presence of applied power



## 3 Connector Pin Out (D15 Plug)

The YED/A429-R1-DAT1-10V contains a single 15-pin filtered male connector, J1, per MIL-C-24308.



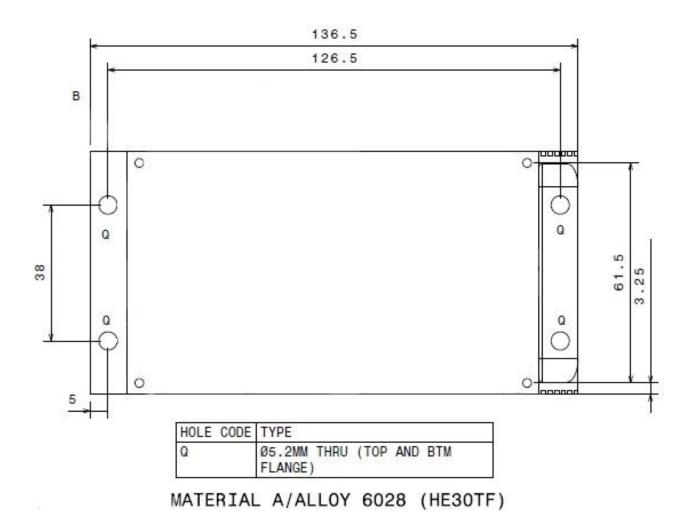
Pin	Signal	Function
1	+28V DC	Primary power
2	0V Ground	28V DC Return
3	A429 Rx +Ve	ARINC 429 Receive (input) RXA
4	A429 Rx -Ve	ARINC 429 Receive (input) RXB
5	A429 Ground (0V)	ARINC 429 Screen (Shield)
6	CH1:+10V O/P	Channel 1, Label A, 0-10V DC output Label 164
7	CH1: 0V O/P	Channel 1 ground / 0V output.
8	Reserved	Reserved
9	Reserved	Reserved
10	Reserved	Reserved
11	Reserved	Reserved
12	Reserved	Reserved
13	Reserved	Reserved
14	Reserved	Reserved
15	Reserved	Reserved

Table 1 – P1 Pin Description

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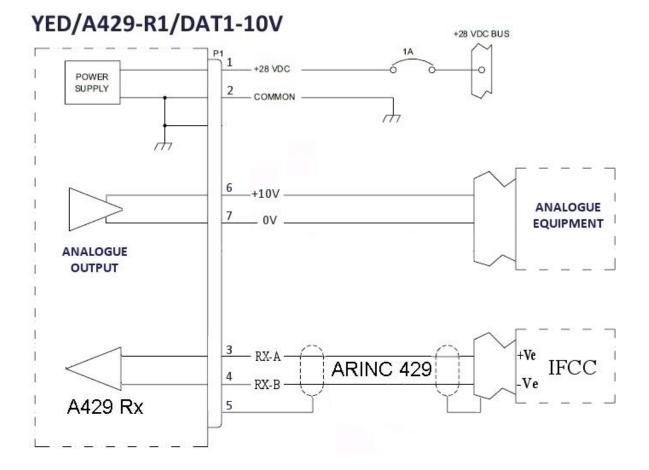
## 4 Enclosure outline drawing



All dimensions are in mm.

## 5 Typical interconnect drawing

A typical equipment interconnect wiring diagram is shown below.



## 6 Installation

This section provides details for the installation of the YED/A429-R1-DAT1-10V Converter, including configuration and mounting procedures. Follow the procedures and recommendations found in this section to assure a successful installation.

#### 6.1 Electrical considerations

A circuit breaker such as a Klixon 7277-2-1 or equivalent should be considered for connecting the power from the aircraft supply to this converter – even though the converter is internally fused.

## 6.2 Materials not supplied

- Wire: MIL-W-22759/16 or equivalent
- Shielded wire: MIL-C-27500 or equivalent
- Mounting Screws, 4 each.

### 6.3 Mounting considerations

The YED/A429-R1-DAT1-10V can be mounted in the avionics bay, shelf or other suitable structure. It can be mounted in any orientation.

#### 6.4 Wiring

Use 22 to 24 AWG wire for all connections.

Fabricate wiring harness, and test all wiring for continuity and for shorts. Ensure aircraft power is present on the correct pins of J1; refer to Table 1.

### 6.5 Removal and replacement

### 6.5.1 Removal

- 1. Open the circuit breaker powering the YED/A429-R1-DAT1-10V.
- 2. Remove the connector.
- 3. Remove four (4) screws securing the converter to the airframe.

## 6.5.2 Replacement

- 1. Open the circuit breaker powering the YED/A429-R1-DAT1-10V
- 2. Secure the converter to the airframe with four (4) screws.
- 3. Attach the connector and secure
- 4. Close the circuit breaker.
- 5. Perform operational test of the YED/A429-R1-DAT1-10V

### 6.6 Continued Airworthiness

### 6.6.1 Scheduled Maintenance

•	Recommended periodic scheduled servicing None
•	Recommended periodic scheduled preventative
	maintenance tests None
•	Recommended periodic inspections None
•	Recommended period overhaul period None
•	Special inspection requirementsNone

There are no Airworthiness limitations associated with the installation of this converter.



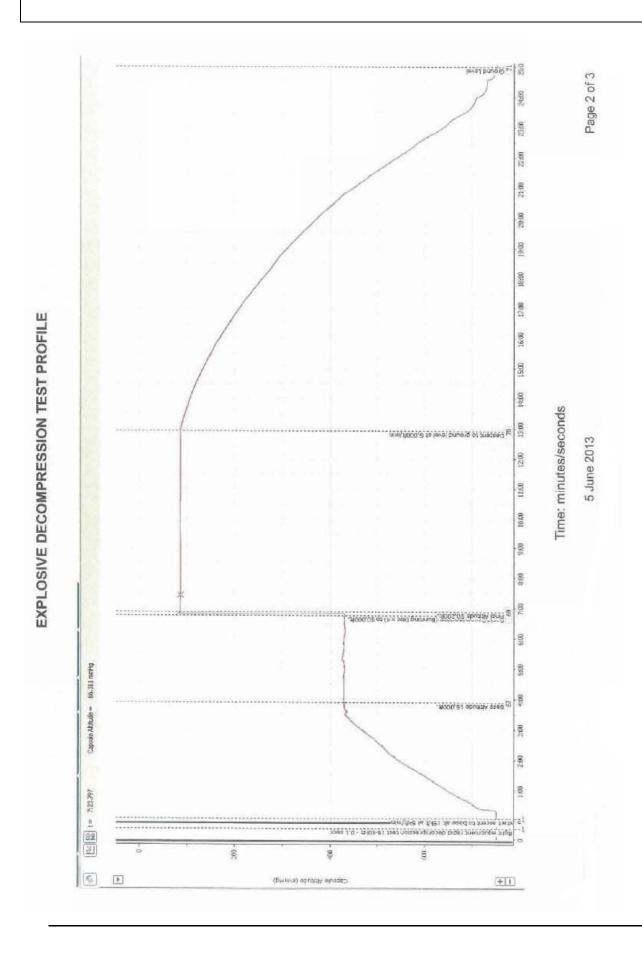
## 7 Environmental & EMC

The YED/A429-R1-DAT1-10V has been designed to meet the environmental test categories detailed below in accordance with RTCA DO-160D, Environmental Conditions and Test Procedure for Airborne Equipment.

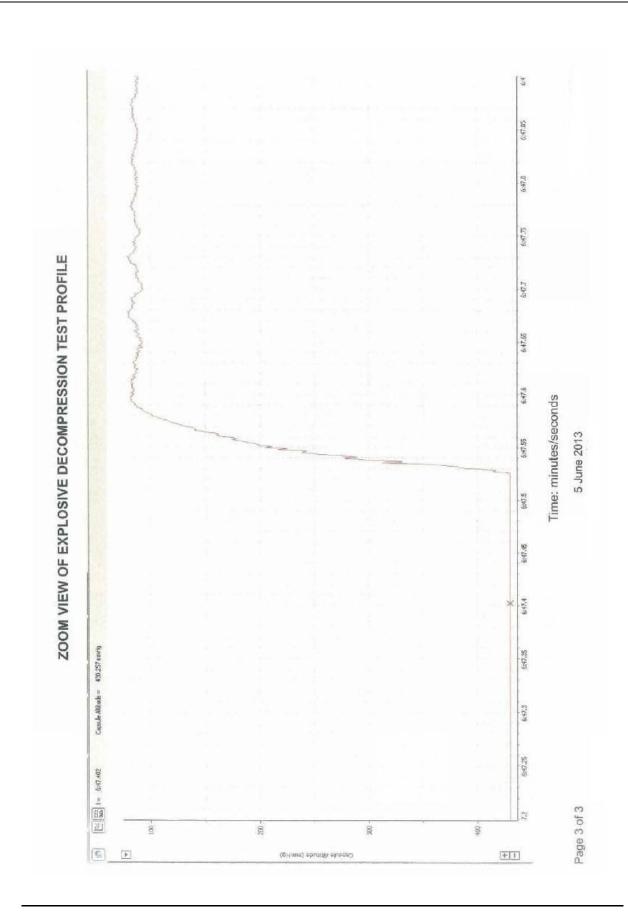
Section	Category	Remarks
4.0 Temperature and Altitude	A1, A2	25,000 feet.
5.0 Temperature and variation	B, C	
6.0 Humidity	A	
7.0 Operational Shock and Crash Safety	В	
8.0 Vibration	C,M	
9.0 Explosion Proofness	X	Not tested – See Annex 1
10.0 Waterproofness	X	Not tested
11.0 Fluids susceptibility	X	Not tested
12.0 Sand and Dust	X	Not tested
13.0 Fungus Resistance	X	Not tested
14.0 Salt Spray	X	Not tested
15.0 Magnetic Effect	Z	
16.0 Power Input	A	
17.0 Voltage Spike	В	
18.0 AF Conducted Susceptibility – Power Inputs	A	
19.0 Induced Signal Susceptibility	A, Z	
20.0 Radio Frequency Susceptibility (Radiated and Conducted)	T, V	
21.0 Emission of Radio Frequency Energy	A, Z	
22.0 Lightening Induced Transient Susceptibility	A,B,Z	Discrete sense pins and ARINC 429 driver output.
23.0 Lightening Direct Effects	X	Not tested
24.0 Icing	X	Not tested
25.0 ESD	X	Not tested

# 8 Annex 1 – Explosive Decompression tests

	DECOMPRESSION TESTING OF	NAVIGATION	UNIT
	QinetiQ Building 800 Hypobaric Facility (Bo decompression testing of a Converter unit. The reason for this testing, in enable clearance to be given for the equipment to	conjunction with other requir	TACAN ed testing, is to
).	The equipment to be tested was supplied	on 22 <sup>rd</sup> May 2013 and com	prised;
	- YED TACAN Converter unit.	Local Serial I	No. 001.
),	Prior to the decompression test, the manufacture supplying the equipment equipment did not require to be functioning during manufacturer for examination post testing.	that the equipment was	representatives serviceable. The returned to the
١.	The decompression test was carried out in the May 2013 to the following decompression to	Hypobaric Chamber (	Building 800) or
	<ul> <li>Chamber ascent to 15,000 ft at a rate of 5</li> <li>Hold for a minimum of 1 minute.</li> <li>Rapid decompression to 50,000 ft (To reatime period).</li> <li>Hold chamber altitude for a minimum of 5</li> <li>Chamber descent to ground level at 5,000</li> </ul>	ch 90% of the final altitude wit minutes.	hin a 0.1-second
i.	The altitude profile within the chamber was recacquisition system. Annex A shows the test prof (428 mmHg), explosive decompression to 50,00 for a minimum of 5 minutes, descent back downlevel to 15,000 ft and descent from 50,000 ft to ft /min. Annex B shows the actual rapid decochamber reached 90% of the final altitude in a 0 altitude, or pressure, within the chamber are countries that the chamber are countries of the pressure recordings show altitude/pressure relationship.	file from; ground level (760 mm 00 ft (87 mmHg) in less than of to ground level. Chamber as of ground level was carried out a sompression in greater detail of .05-second time period. The rantrolled in units of feet per min	nHg) to 15,000 f 0.1 second, hold cent from ground at a rate of 5,000 during which the stes of change on the (ft/min). Fo
).	The chamber decompression test proceeded without any effect on the equipment. The B equipment to the manufacturer for examination.		
	This documented detail of the testing has been (Boscombe		Division).



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